

Tenders are invited for the up gradation of an existing 32 cores cluster for the scientific calculations. The applications of the cluster are to run the plane wave and molecular dynamics simulations under ongoing DST and UGC projects. The detail specification and terms and conditions are given below.

A. Dual CPU Intel Xeon Based Node: Intel Xeon E 5420 Based.

Quantity: 2 nodes,

Number of Cores per node/server: 8

CPU: 2x intel Xeon 5420 (2 quad core CPU support) Xeon/Quadcore/2.50 Ghz/1333MHz

FSB/12 MB

NIC : Intel (ESB2/Gifgal) 82563EB Dual –port Gigabit Ethernet controller

On Board Infiniband Mellanox x MT25204 ADFCCD Single port

Form factor: 2 units in a 1 U chains, Telescope sliding rail kit

B. RAM : 8 GG ECCFB-DIMM DDR II 667, 4 GBx2 modules

HDD 1x250 GB SATA HDD RPM 4 port SAS Controller, 2x SATA 3.0 Gbps ports via ESB2 Controller

C. IPME card FOR THE ABOVE NODES- 2 Nos.

This quote should include installation and optimization for the application software and OS, Compilers, libraries as well as configuration of the new nodes.

Terms and Conditions:

1. FOR : DOP, Bhavnagar University, Bhavnagar 364 002
2. Prices should be inclusive of all taxes , transportation charges, etc.
3. For the cores, please quote per core.
4. Quotation from those firms will only be accepted which have at least experience of three installation of the clusters (certificate is required)
5. EMD RS. 3,000/- has to be deposited along with tender by DD, in favour of Prof. P.K. Jha, PI, UGC project.
6. Subject to Bhavnagar University Rules and Regulations
7. All rights to accept/partly accept/reject/modify lie with the Bhavnagar University.
8. The prices should be valid at least for the 30 days.